

■ INTRODUCTION

The Option 39S – Surge Current Test Terminal may be used with SemiTek's 201C Discrete Semiconductor Test System to provide a surge current for testing Thyristor-type devices. The test terminal is configured as a stand alone and is interconnected to the main station using a six (6) foot cable assembly and the 201-074 Multiplexer as necessary.

■ SPECIFICATIONS

- Wave Forms

Typical wave form types include:

- Exponential Decay Current, "Y" and "Z" in uSec.
- Square Wave, Line Frequency.
- Half Sine Wave, Line Frequency.
- Rectified Sine Wave, Line Frequency.

Note: Wave Form desired is be customer-predefined.

- Iterations

One or more iterations of a single surge may be performed with sufficient charge time in-between.

- Charge Time

Charge Time is one (1) second. Additional times are available subject to engineering review.

- Current

Peak Current is programmable to 250A in 3 digit steps. Peak current is detected but not measured insuring the programmed value is reached.

- Voltage

Peak Voltage is measured to an accuracy of +/- 1.5%. Bias Voltage is optional.

- Rise/Fall Time

Selectable for exponential decay wave form. Time tolerance is +/-10%. Selectable wave forms are to be customer-predefined.

- Direction

Surge may be performed in forward or reverse direction, or both with time for recharge.